| | Application No. | Applicant(s) | _ |
|---|---|--|---------------------------|
| Alada - E Allana L'194 | 10/707,123 | BONACCIO ET AL | |
| Notice of Allowability | Examiner | Art Unit | |
| | DINH T. LE | 2816 | |
| The MAILING DATE of this communication apper All claims being allowable, PROSECUTION ON THE MERITS IS herewith (or previously mailed), a Notice of Allowance (PTOL-85) NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RI of the Office or upon petition by the applicant. See 37 CFR 1.313 | (OR REMAINS) CLOSED in this app or other appropriate communication GHTS. This application is subject to | plication. If not include will be mailed in due | ed course. THIS |
| 1. This communication is responsive to <u>5/25/05</u> . | | | |
| 2. The allowed claim(s) is/are 2-14,16-28 and 30-42. | | | |
| 3. The drawings filed on 21 November 2003 are accepted by the Examiner. | | | |
| 4. | | | |
| Attachment(s) 1. ☑ Notice of References Cited (PTO-892) 2. ☐ Notice of Draftperson's Patent Drawing Review (PTO-948) 3. ☐ Information Disclosure Statements (PTO-1449 or PTO/SB/0 Paper No./Mail Date 4. ☐ Examiner's Comment Regarding Requirement for Deposit of Biological Material | 5. Notice of Informal P 6. Interview Summary Paper No./Mail Dat 7. Examiner's Amendr 8. Examiner's Stateme 9. Other | (PTO-413), te ment/Comment | ŕ |

REASONS FOR ALLOWANCE

The following is an Examiner's Statement of Reasons for Allowance:

The applicant's amendment filed 5/25/05 overcame the technical deficiencies and the prior art rejection. Claims 2-14, 16-28 and 30-42 are allowed because the prior art of record does not show an additional capacitor coupled between the first node and a voltage source as combined in claims 3, 17 and 31, the compensation circuit comprising a current source, a sensing element, a voltage buffer, an operational amplifier, a time delay circuit or the digital to analog converter as combined in the allowed claims 8, 10, 11, 22, 24,25, 56, 38 and 39.

CONCLUSION

Any comments considered necessarily by applicant must be submitted no later than the payment of the Issue Fee and, to avoid processing delays, should preferably accompany the Issue Fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

Any inquiry concerning this communication or earlier communications from the examiner should be directed to DINH T. LE whose telephone number is (571) 272-1745. The examiner can normally be reached on Monday-Friday (8AM-7PM).

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, TIMOTHY CALLAHAN can be reached at (571) 272-1740.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

PRIMARY EXAMINER